

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

1. (Canceled)
2. (Currently amended) ~~The semiconductor integrated circuit device according to claim 1,~~
wherein A semiconductor integrated circuit device comprising:
a plurality of internal power supply generating circuits arranged on a single chip; and
a common monitor pad;
wherein the internal power supply generating circuits are connected via respective
switches to the common monitor pad,
the internal power supply generating circuits and the monitor pad are selectively
connectable by the switches, and
the internal power supply generating circuits generate equal internal power supply voltages.
3. (Currently amended) The semiconductor integrated circuit device according to claim [[1]]
2, wherein each of the internal power supply generating circuits generates an internal power supply based on an external power supply.
4. (Currently amended) The semiconductor integrated circuit device according to claim [[1]]
2, wherein all of the switches are capable of being turned off at the same time, and each of the switches is capable of being turned on or off selectively.
5. (Currently amended) The semiconductor integrated circuit device according to claim [[1]]
2, wherein each of the switches is an N-channel transistor or a P-channel transistor.
6. (Currently amended) ~~The semiconductor integrated circuit device according to claim 1,~~
wherein A semiconductor integrated circuit device comprising:

a plurality of internal power supply generating circuits arranged on a single chip; and
a common monitor pad;
wherein the internal power supply generating circuits are connected via respective
switches to the common monitor pad.
the internal power supply generating circuits and the monitor pad are selectively
connectable by the switches, and
the internal power supply generating circuits are capable of being all or selectively
brought into a deactivated state.

7. (Original) The semiconductor integrated circuit device according to claim 6, further
comprising driver control portions connected to the internal power supply generating circuits,
wherein the driver control portions control a supply of a voltage to the internal power
supply generating circuits, the voltage being input via an external pad.

8. (Currently amended) The semiconductor integrated circuit device according to claim 7,
wherein the driver control portions are provided respectively for the internal power supply
generating circuits, and the ~~foreable application~~ driver control portions and the switches are
controlled by a common control signal.

9. (Currently amended) ~~The semiconductor integrated circuit device according to claim 1,~~
~~further comprising voltage level shifting circuits between the internal power supply generating~~
~~circuits and the switches,~~

wherein A semiconductor integrated circuit device comprising:
a plurality of internal power supply generating circuits arranged on a single chip;
a common monitor pad; and
voltage level shifting circuits between the internal power supply generating circuits and
the switches;
wherein the internal power supply generating circuits are connected via respective
switches to the common monitor pad.
the internal power supply generating circuits and the monitor pad are selectively
connectable by the switches, and

the voltage level shifting circuits shift voltage levels of internal power supplies generated in the internal power supply generating circuits.